

ABSTRACT OF THE DISCLOSURE

5 A single transistor device is configured of a plurality of transistor cells divided and arranged in a plurality of blocks. Corresponding to the blocks a plurality of bias current supply circuits are arranged, respectively, to supply the blocks with individual bias currents, respectively. The bias current supply circuits each have a transistor with a bias condition set to decrease its ability to drive current as the corresponding bias current increases. Thus a negative feedback can be given to an increase in bias current attributed to thermal unevenness.